

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	580	((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)	US-PGPU B; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/19 17:02
L2	37	compar\$3 adj3 ((normal acceptable good) adj3 ranges) with output	US-PGPU B; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/19 17:05
S2	374	((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer))	US-PGPU B; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/28 21:01
S3	366	(((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer)) and (control\$4 logic processor microprocessor))	US-PGPU B; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/28 21:02
S4	338	(((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer)) and (control\$4 logic processor microprocessor)) and reference	US-PGPU B; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/28 21:03

S5	8	(((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer)) and (control\$4 logic processor microprocessor)) and reference) and ((digital adj output\$1) near5 compar\$3)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/28 21:29
S6	31	(((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer)) and (control\$4 logic processor microprocessor)) and reference) and ((digital adj output\$1) same compar\$3)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/28 21:32
S7	19	(((((plurality two) adj3 (analog\$2 adj2 inputs)) same (multiplexer switch\$3 transistor)) and ((analog adj2 digital) quantizer digitizer)) and (control\$4 logic processor microprocessor)) and reference) and ((digital adj output\$1) same compar\$3)) and "341"/\$	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/29 12:41
S8	11	((sequence code program) near5 (stor\$3 memory)) same ((digital adj2 output) with compar\$3) and "341"/\$	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/29 12:54
S9	13	((sequence code program) near5 (stor\$3 memory register latch)) same ((digital adj2 output) with compar\$3) and "341"/\$	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/29 20:08

S10	18	"5416409" and reference	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/29 20:17
S11	17	"5416409" and reference and (storage memory register)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2004/09/29 20:17